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FIG. 1

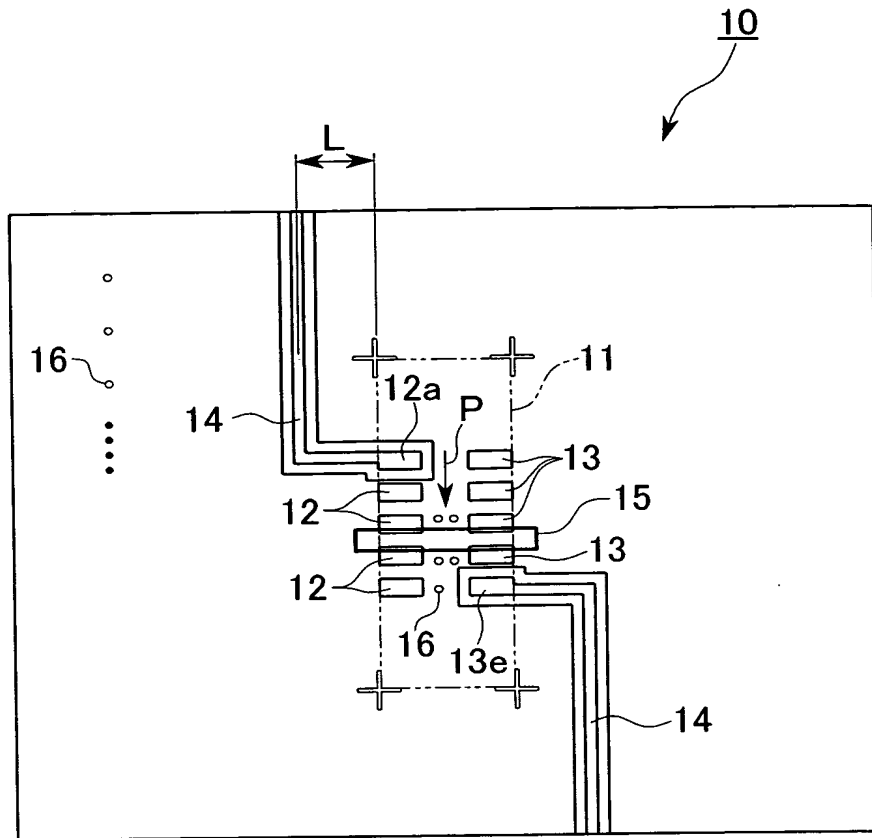
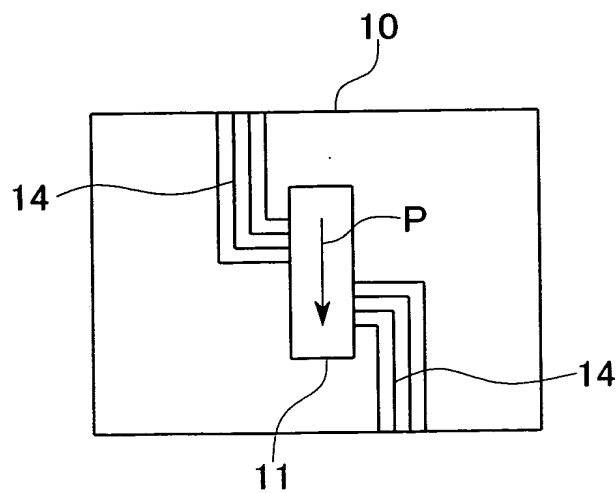


FIG. 2



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FIG. 3

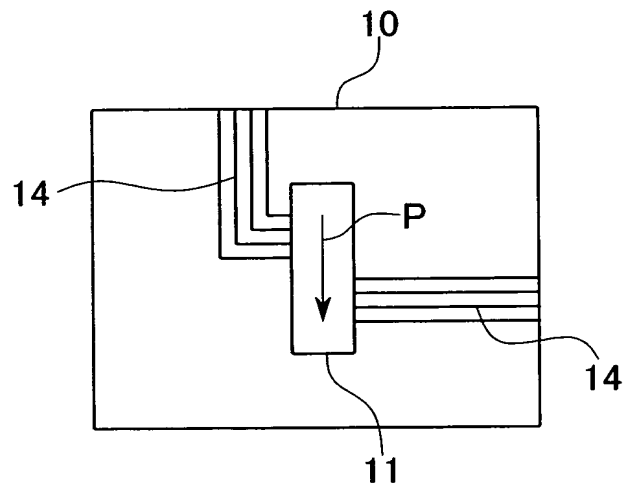
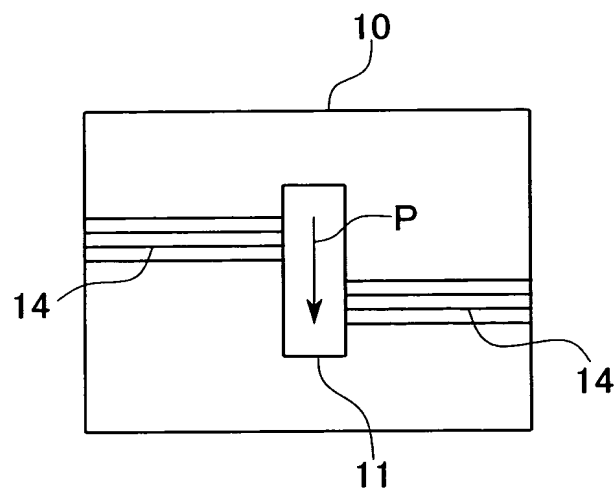


FIG. 4



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FIG. 5

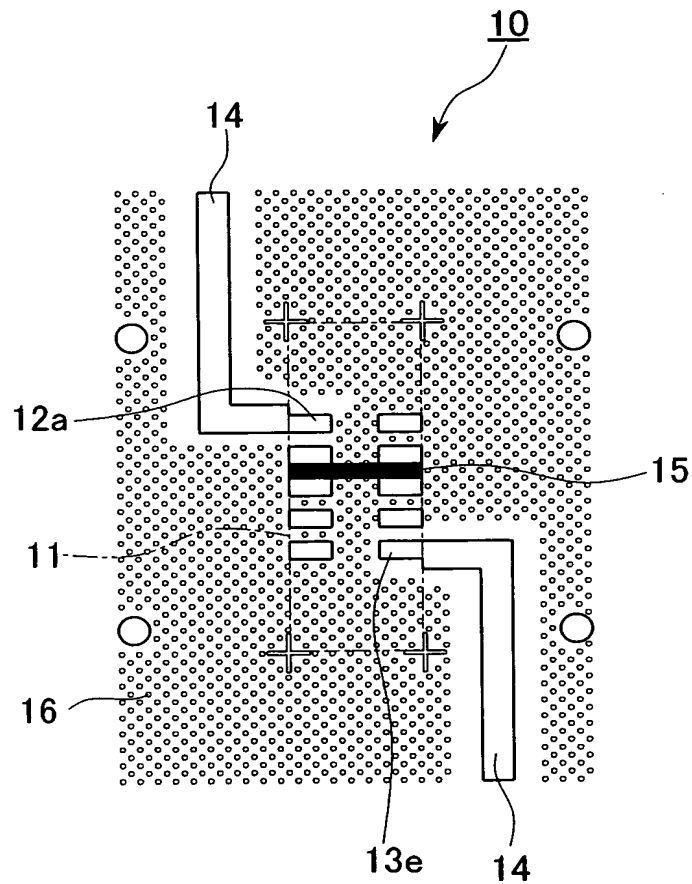




FIG. 7

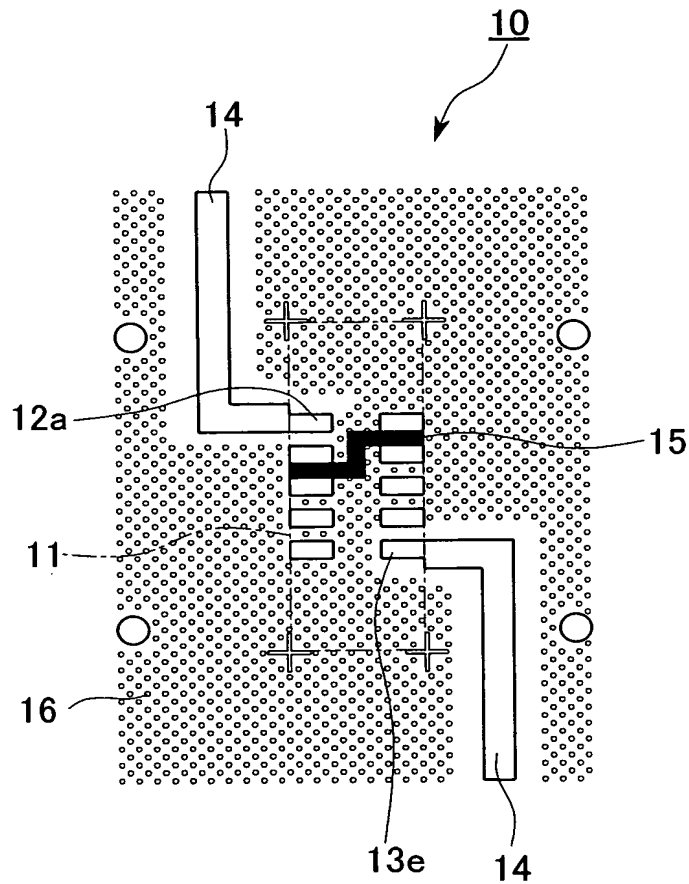


FIG. 8

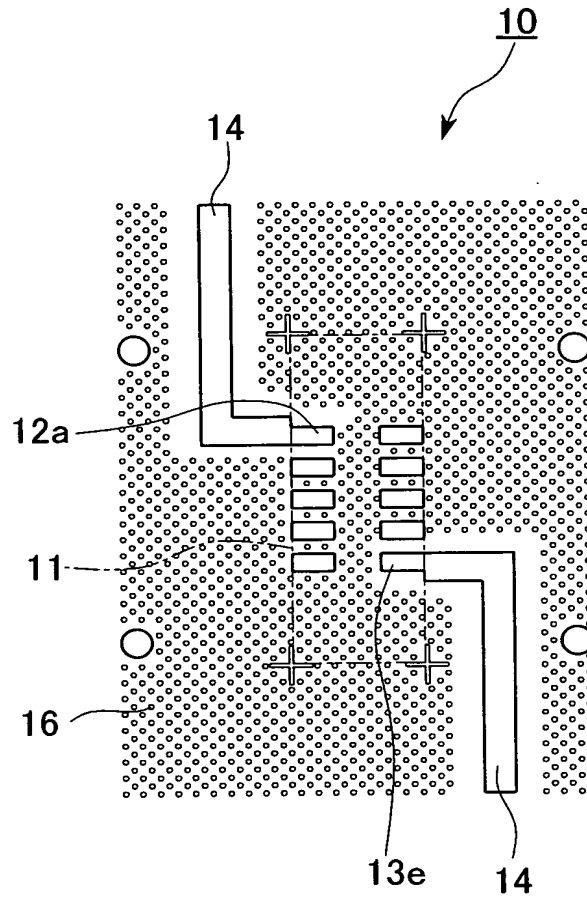


FIG. 9

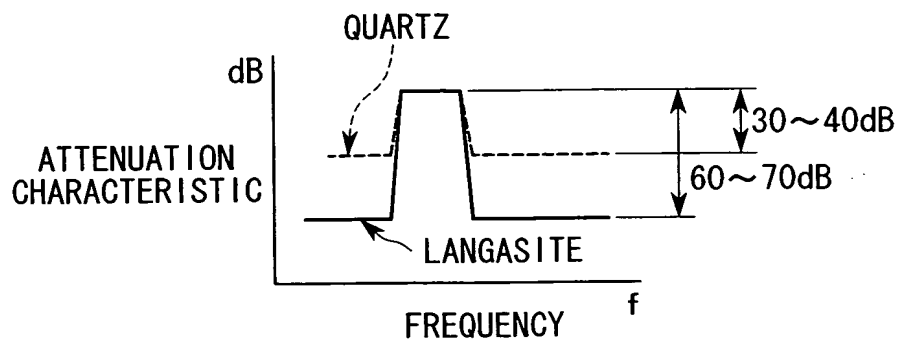


FIG. 10

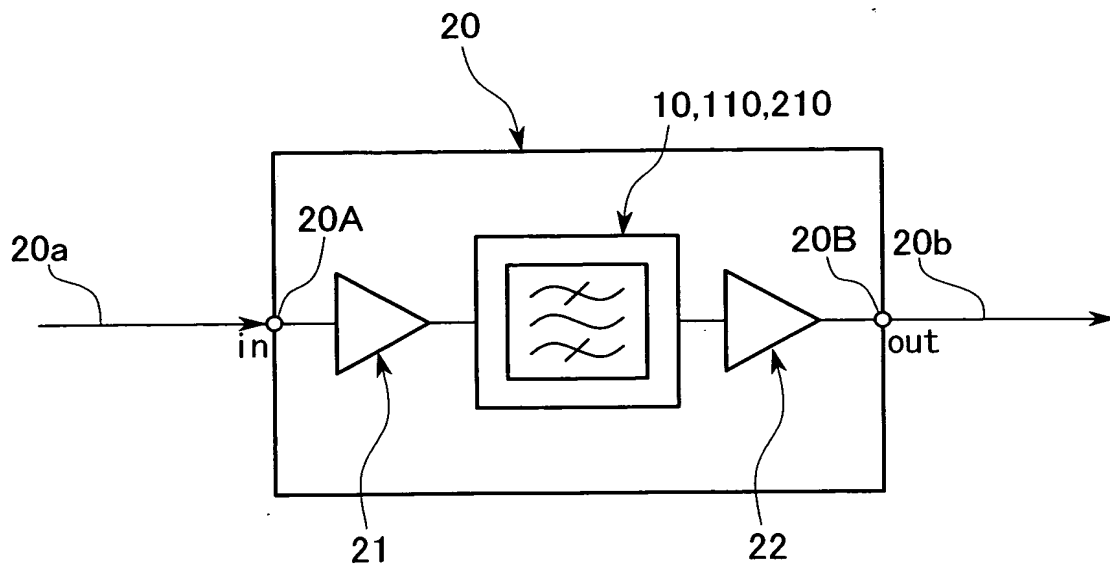


FIG. 11

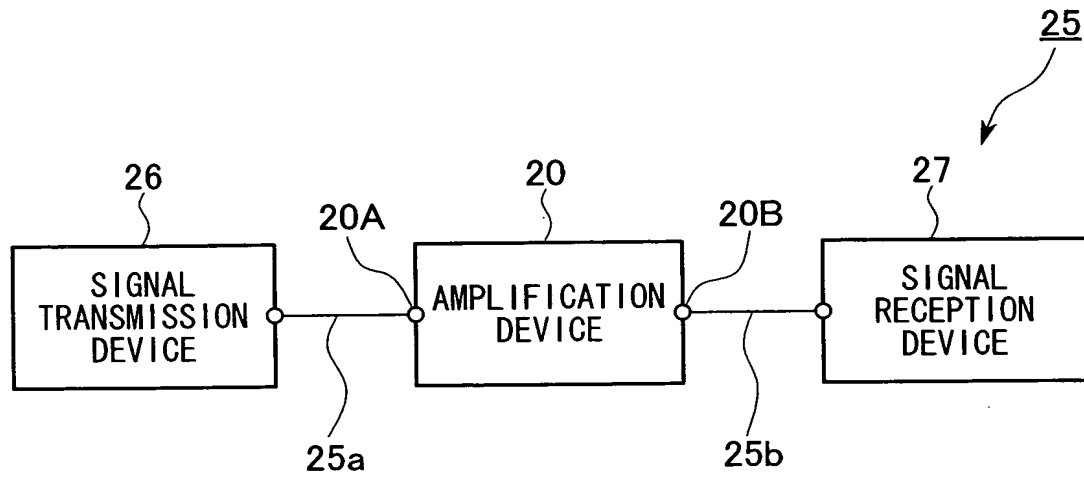


FIG. 12

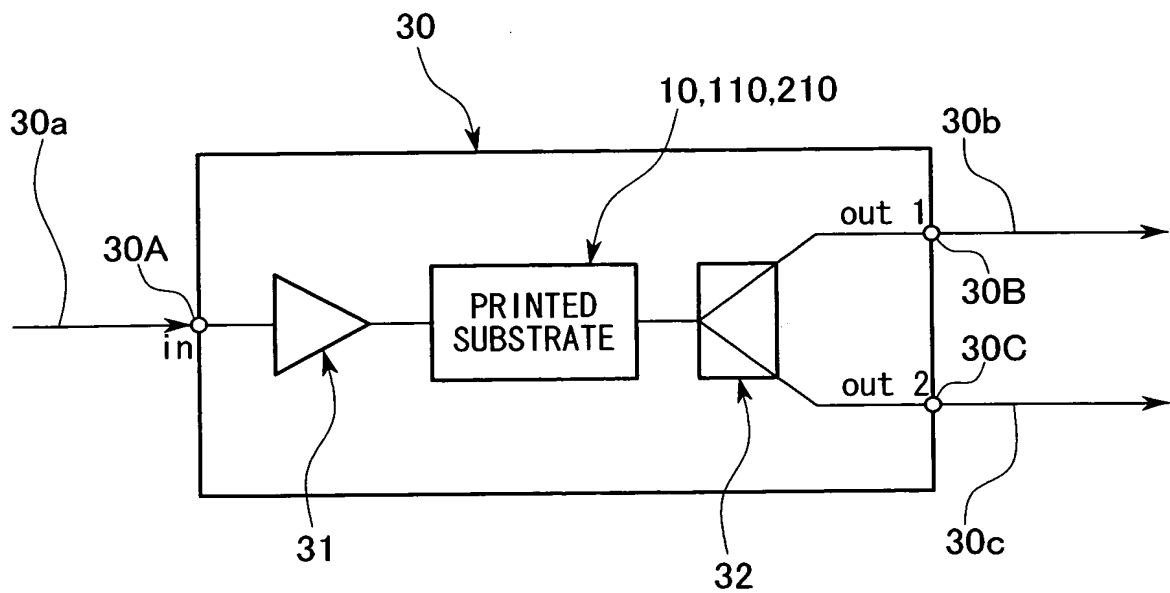




FIG. 13

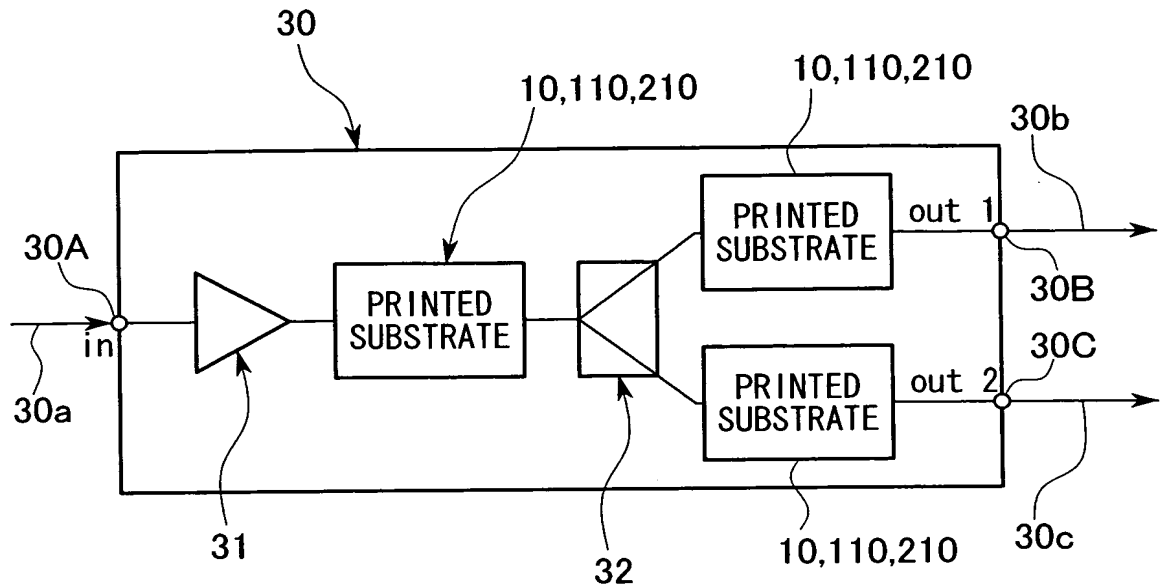


FIG. 14

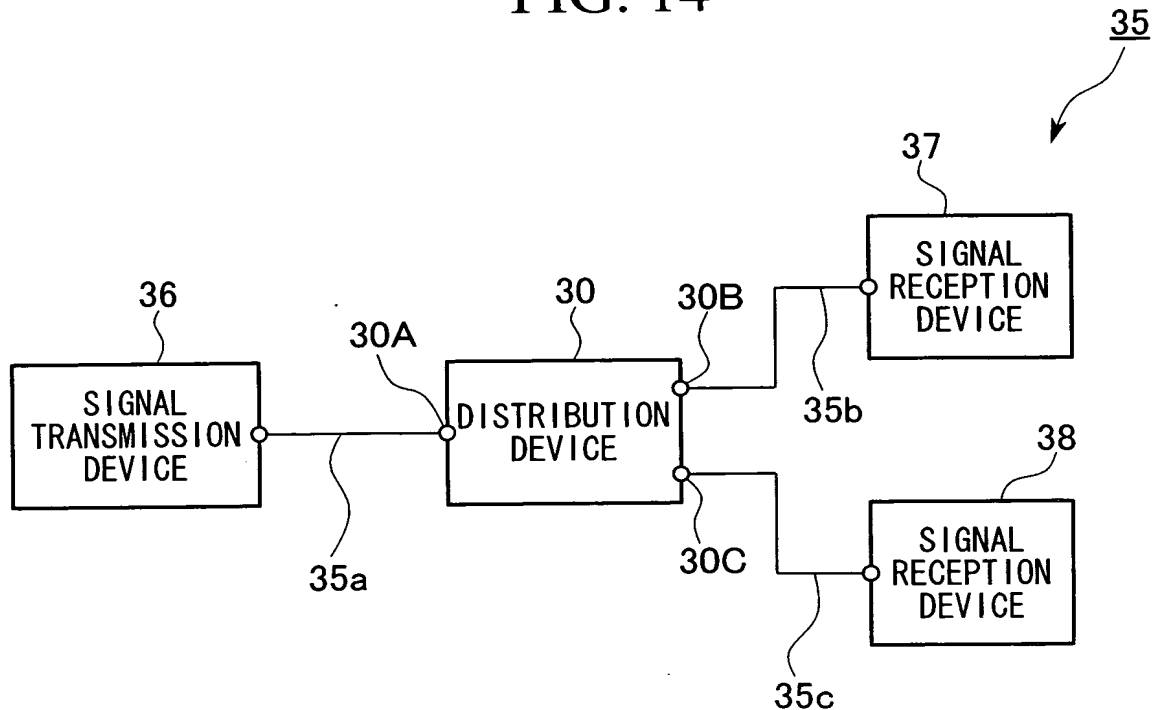


FIG. 15

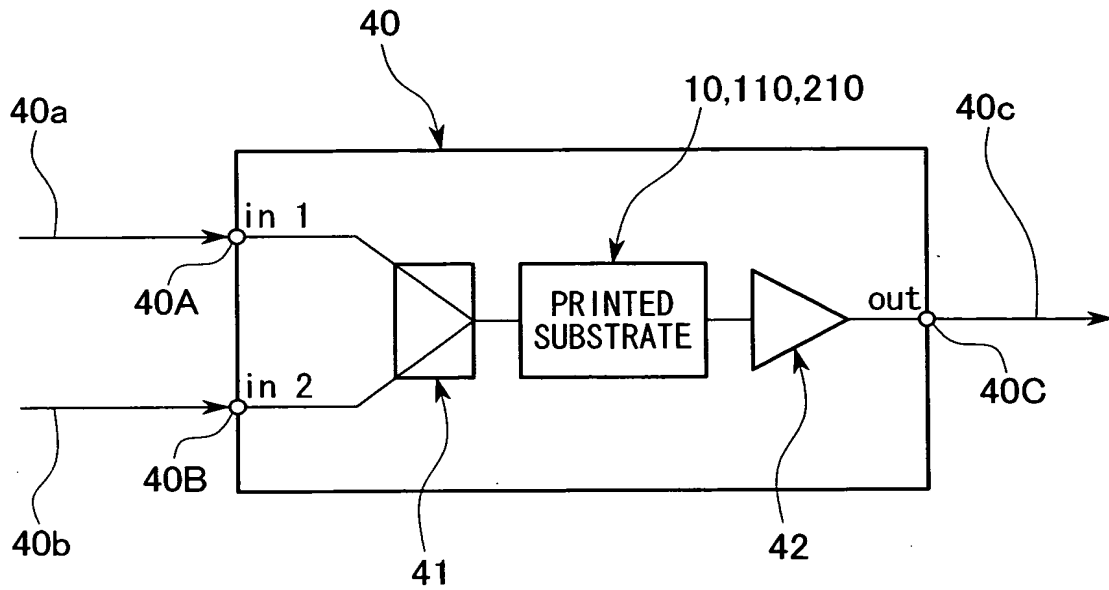


FIG. 16

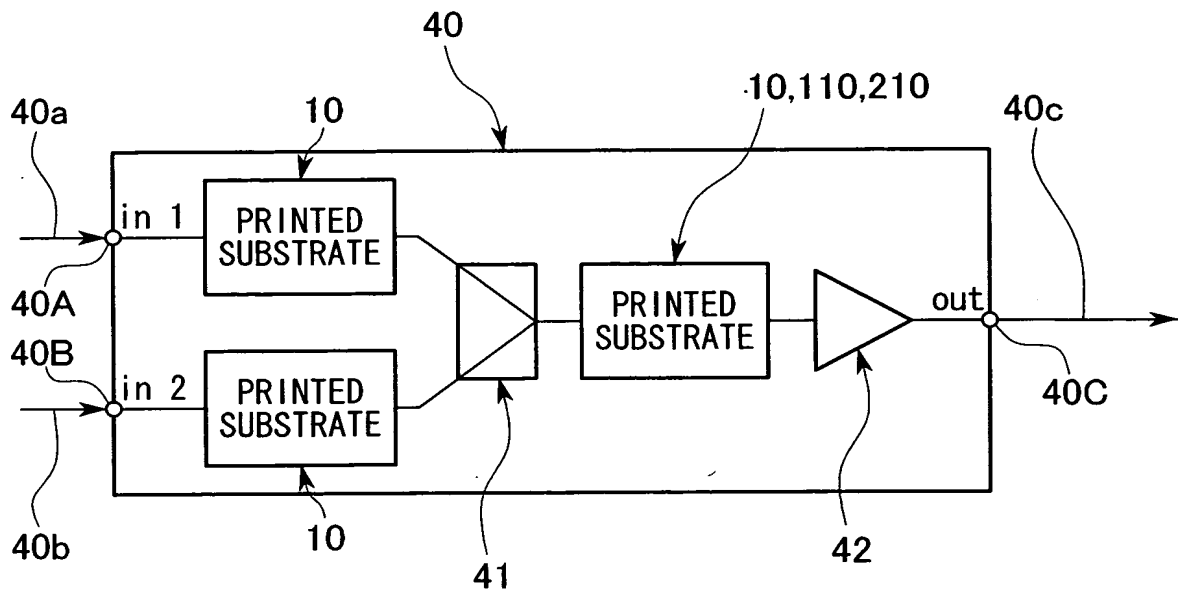


FIG. 17

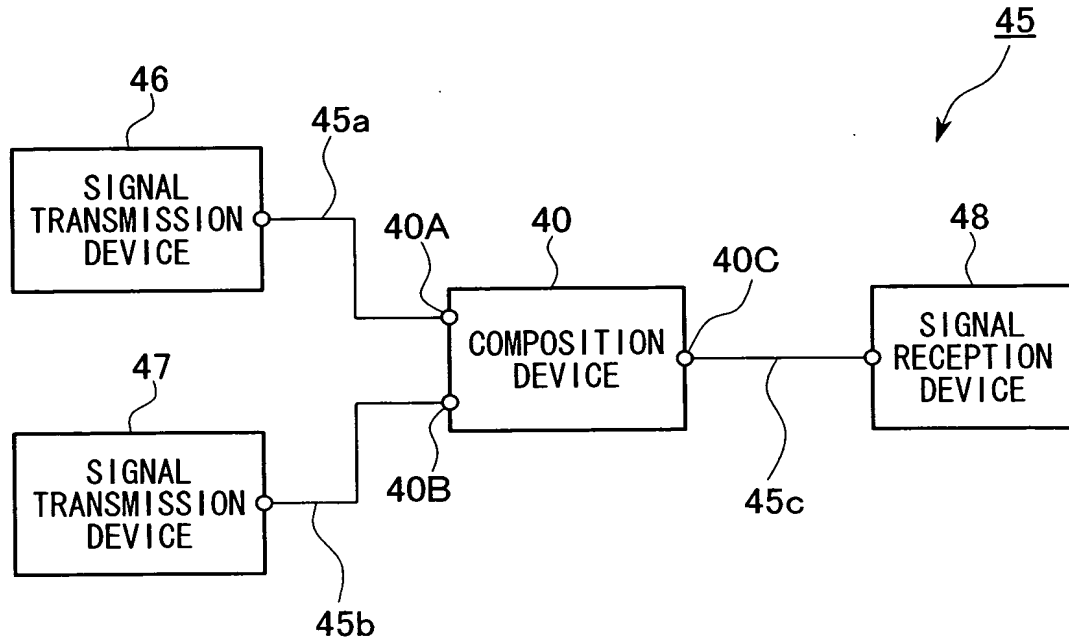
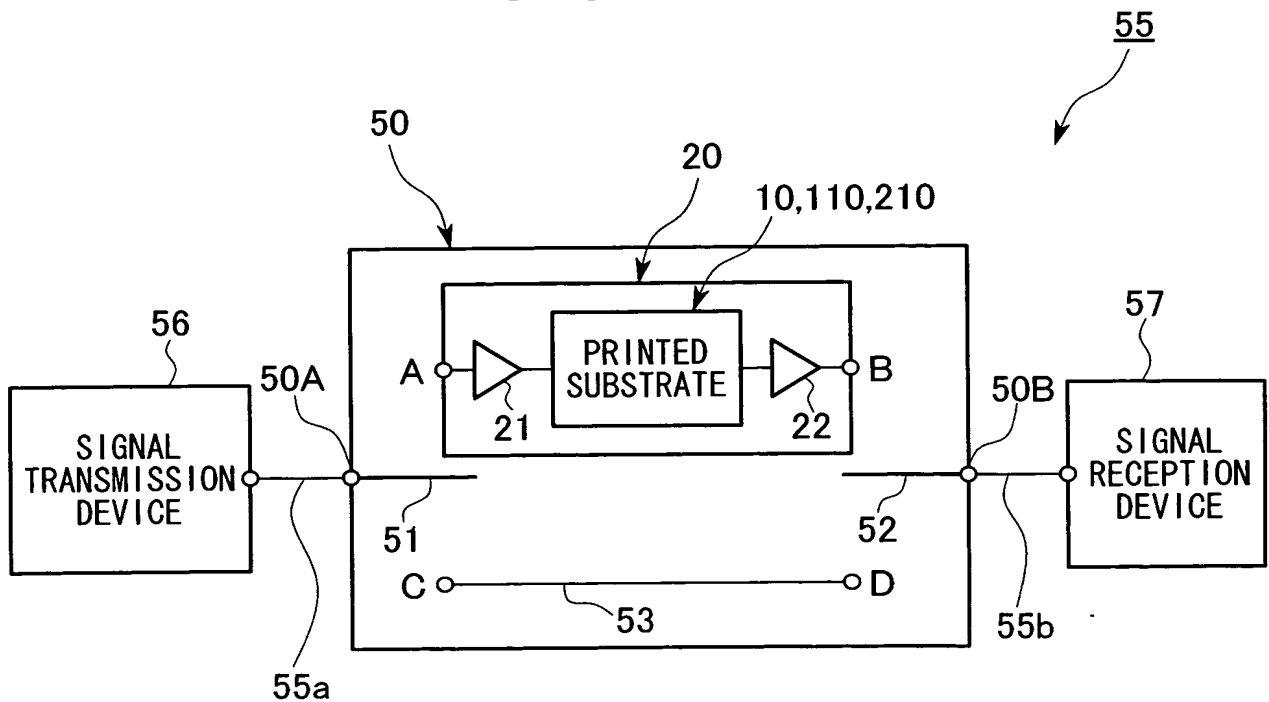


FIG. 18



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FIG. 19

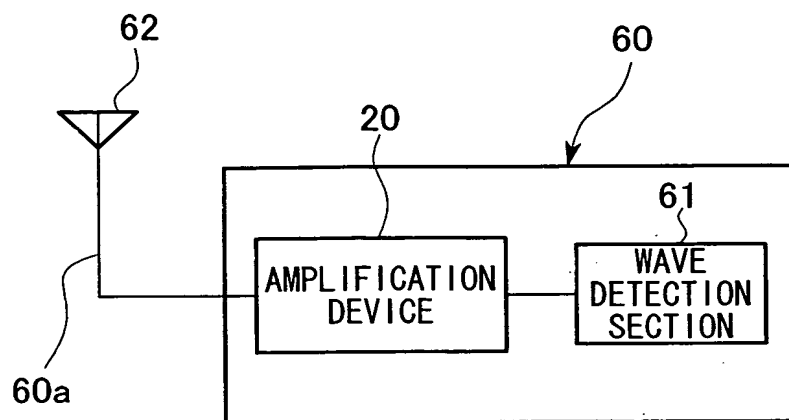


FIG. 20

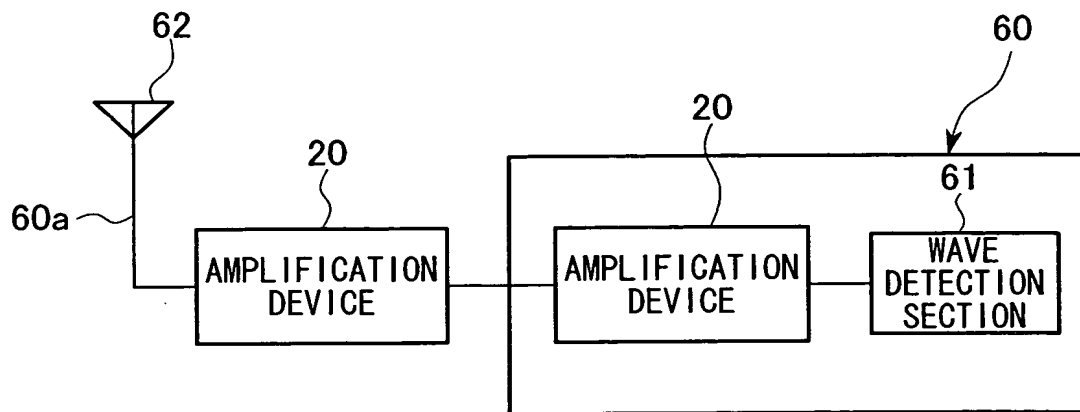


FIG. 21

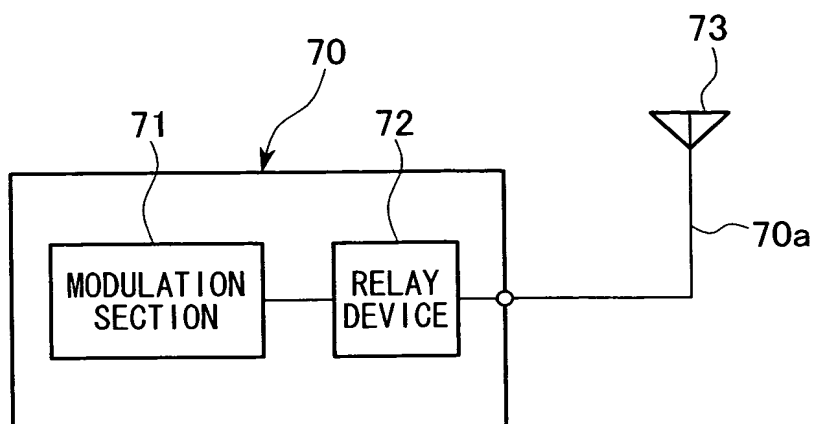


FIG. 22

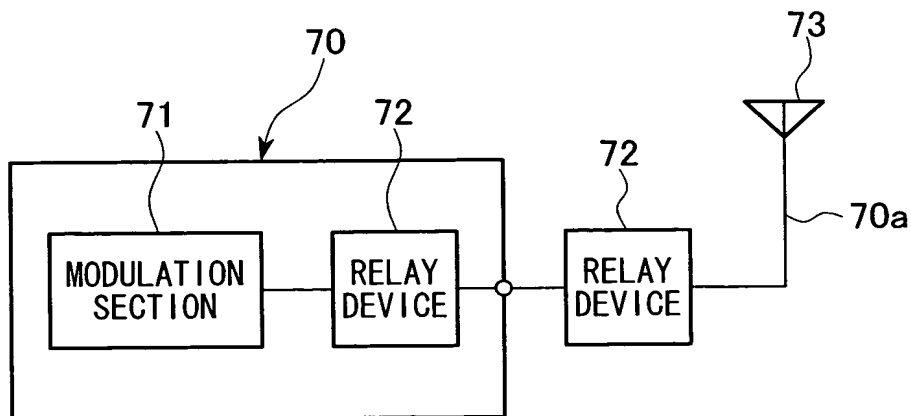


FIG. 23

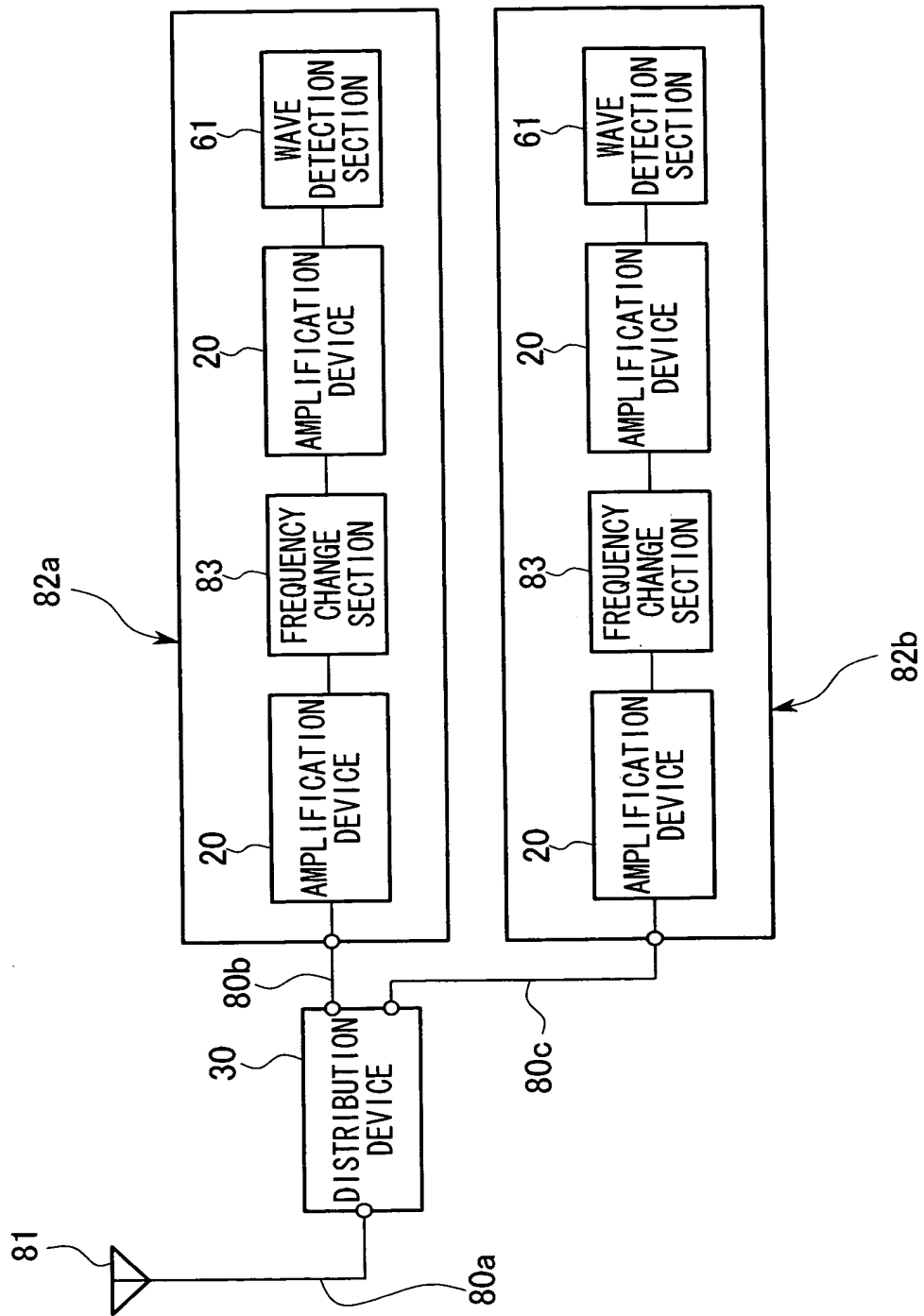


FIG. 24

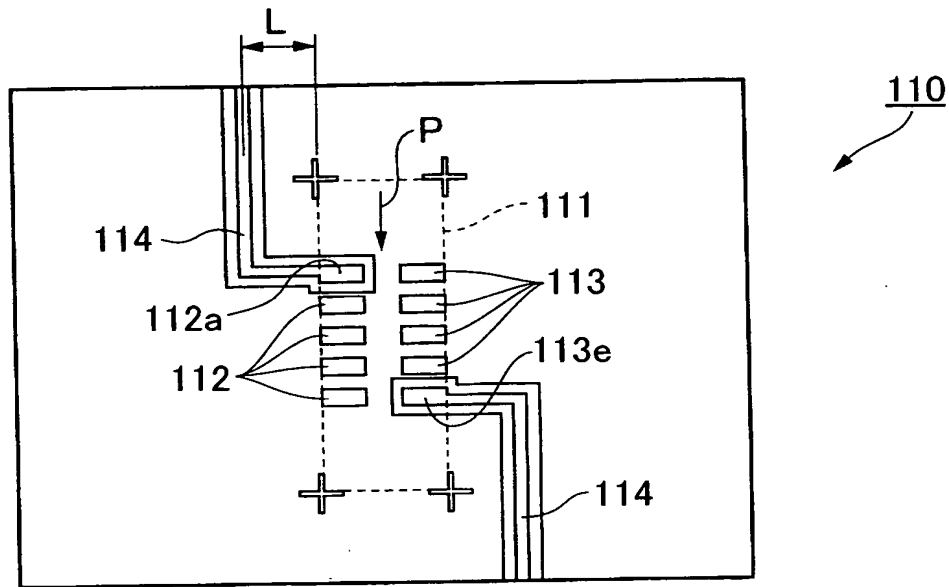


FIG. 25

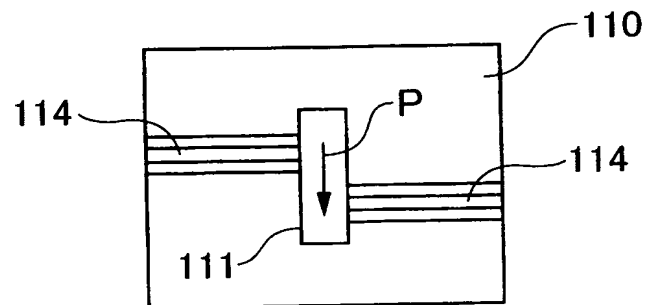


FIG. 26

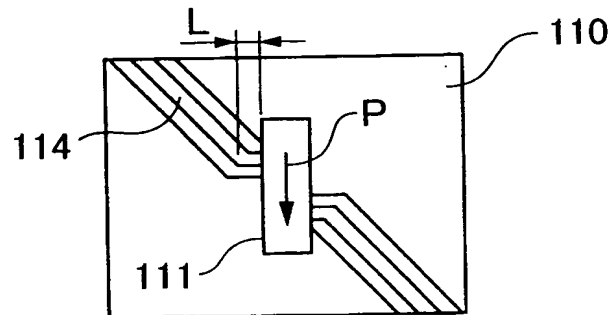


FIG. 27

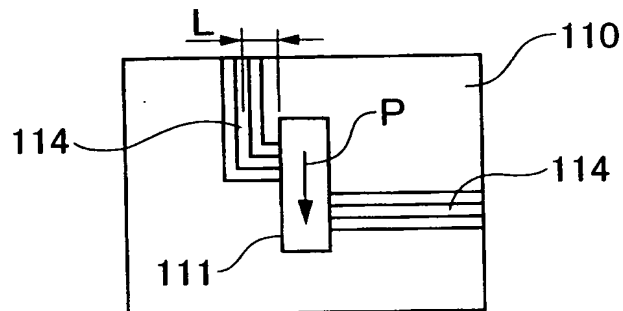


FIG. 28

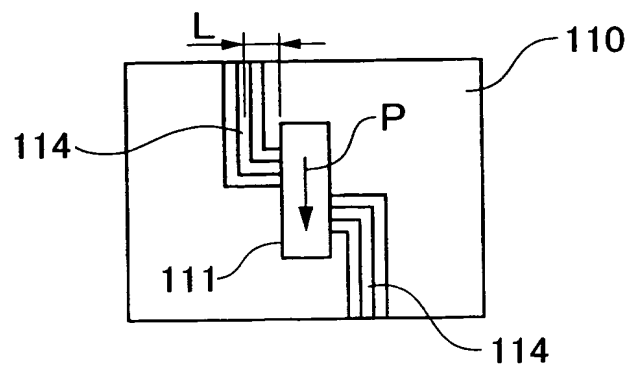




FIG. 29

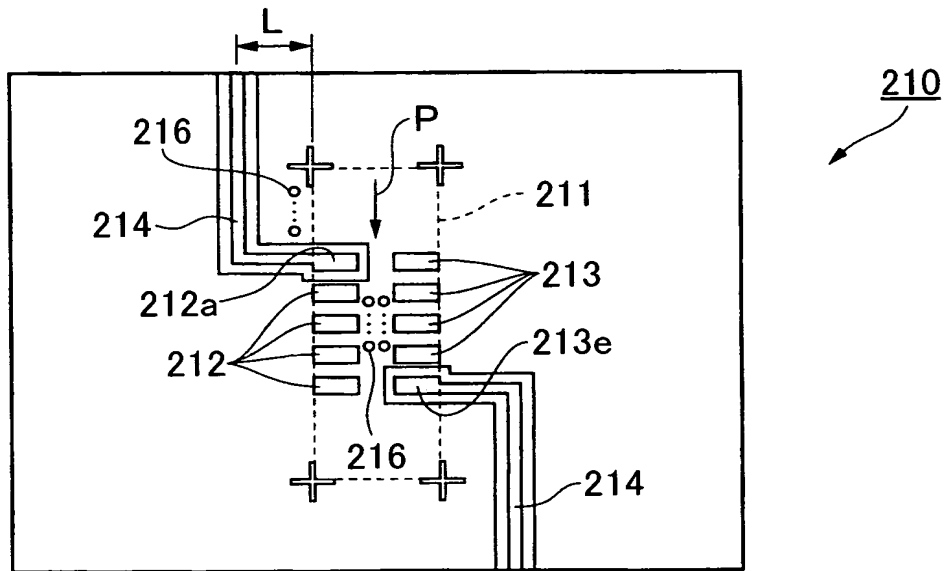


FIG. 30

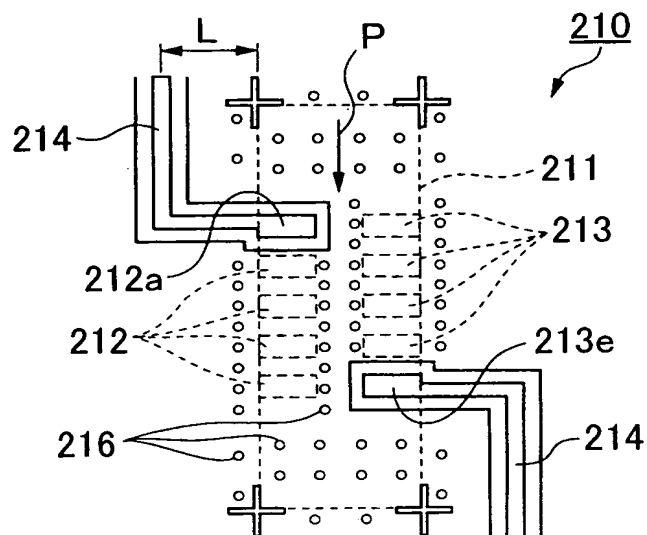


Figure 2 is a plan view of a semiconductor device 210. The device features a grid of elements 211 and 213. A pitch P is indicated between the elements. A length L is indicated for a specific feature. Various interconnects are shown, including 212, 212a, 213e, and 214. The device is labeled 210.

FIG. 33

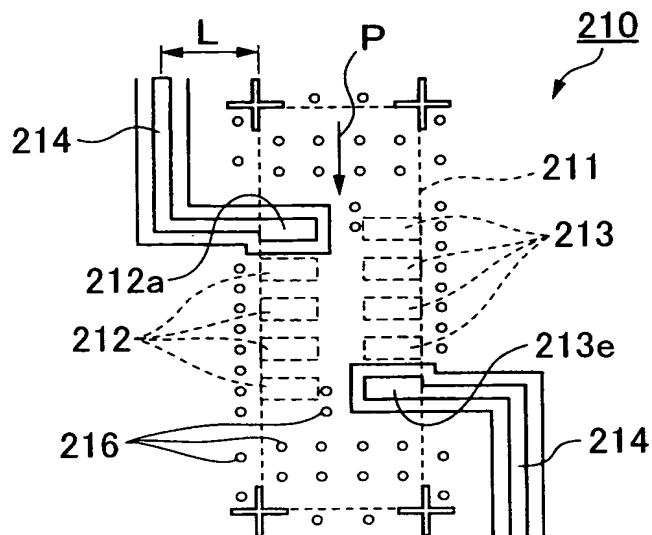
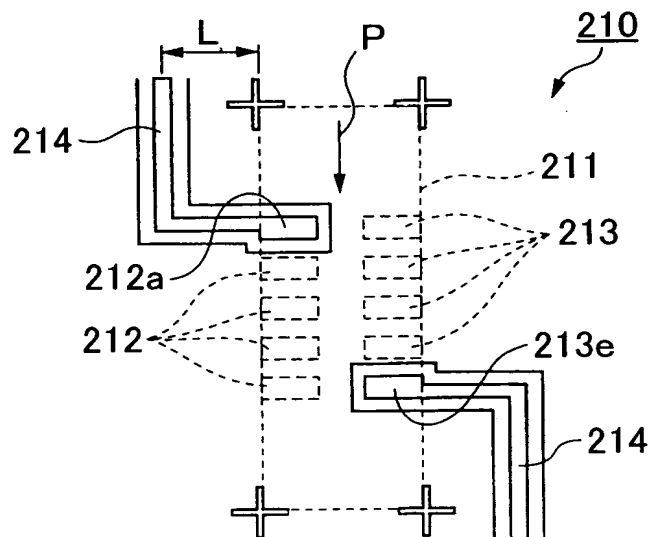


FIG. 34



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FIG. 35

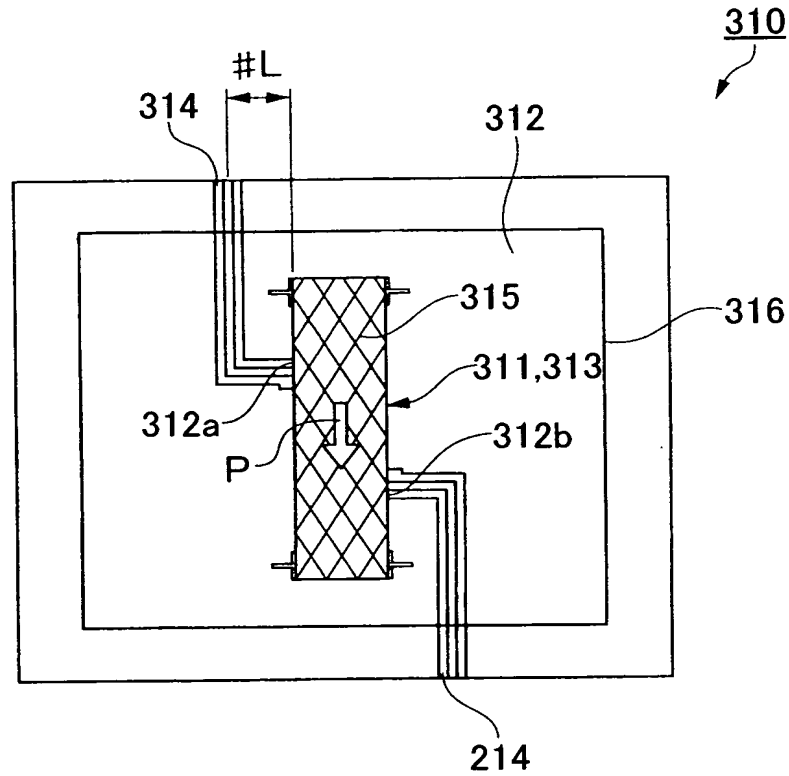
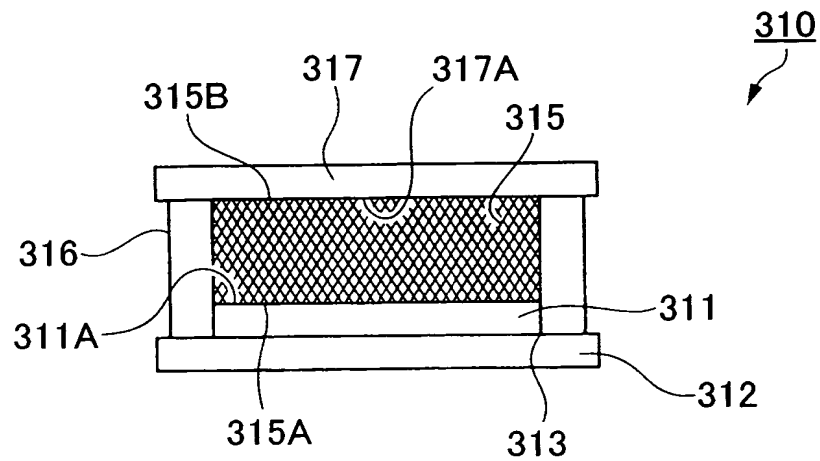


FIG. 36



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FIG. 37A

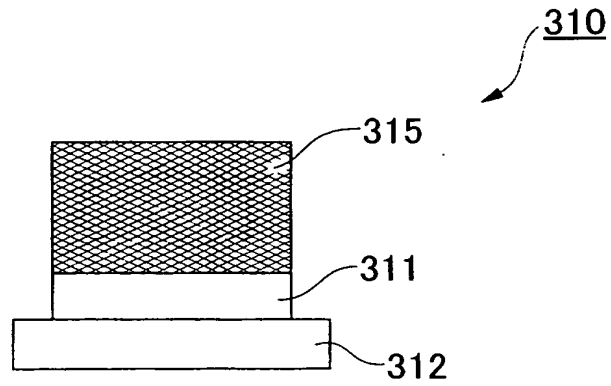
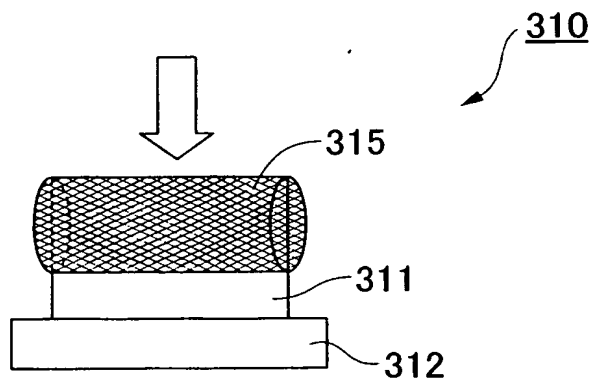


FIG. 37B



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FIG. 38

COMPARISON EXAMPLE 6

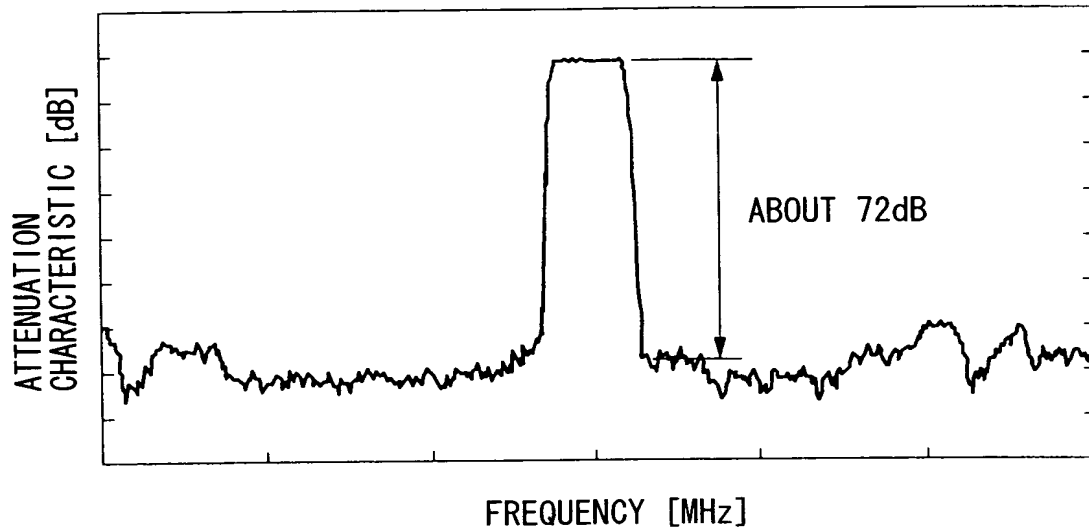


FIG. 39

COMPARISON EXAMPLE 7

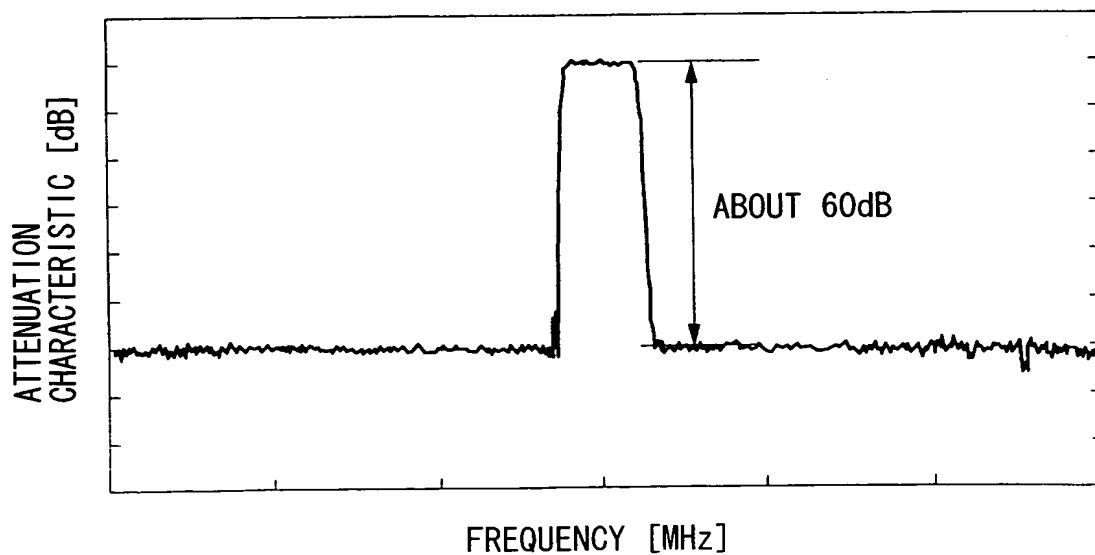
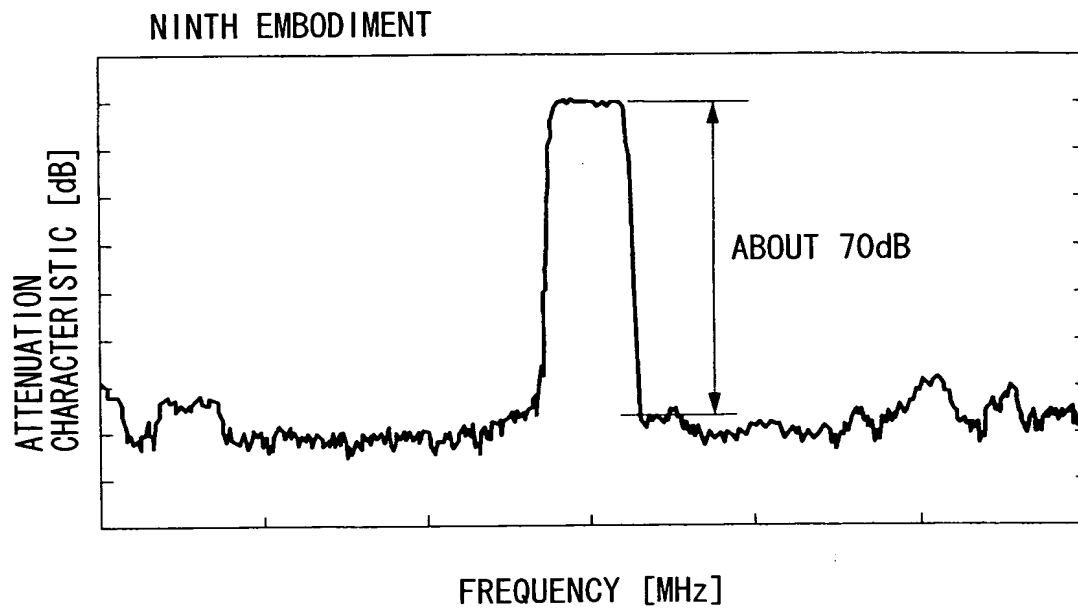


FIG. 40



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FIG. 41A

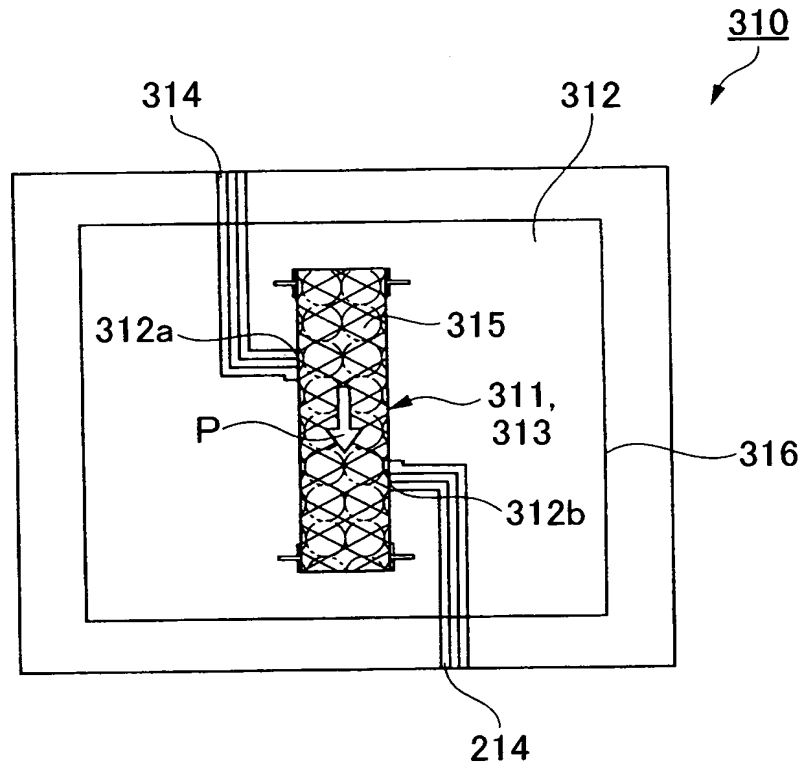
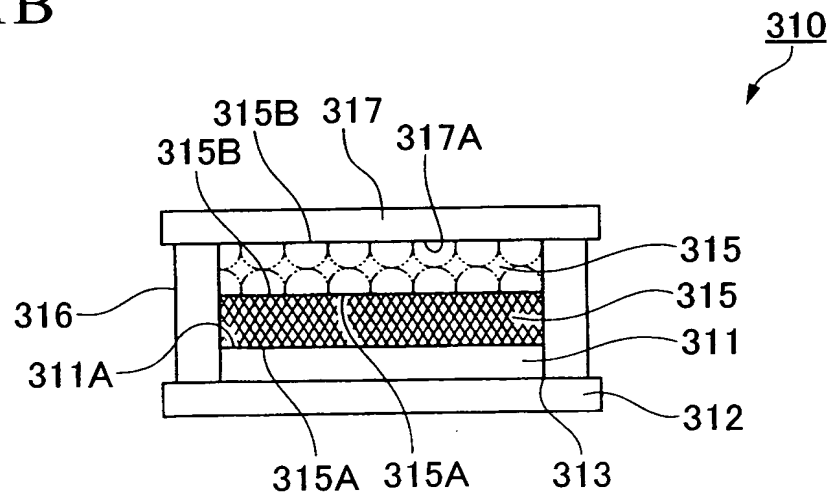


FIG. 41B





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FIG. 42

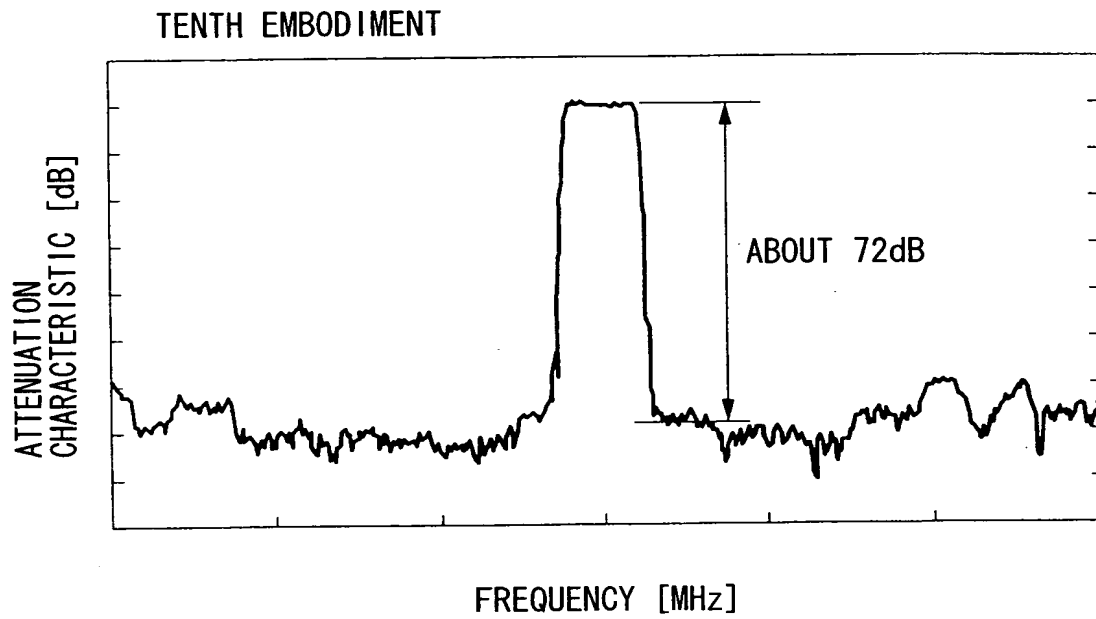


FIG. 43A

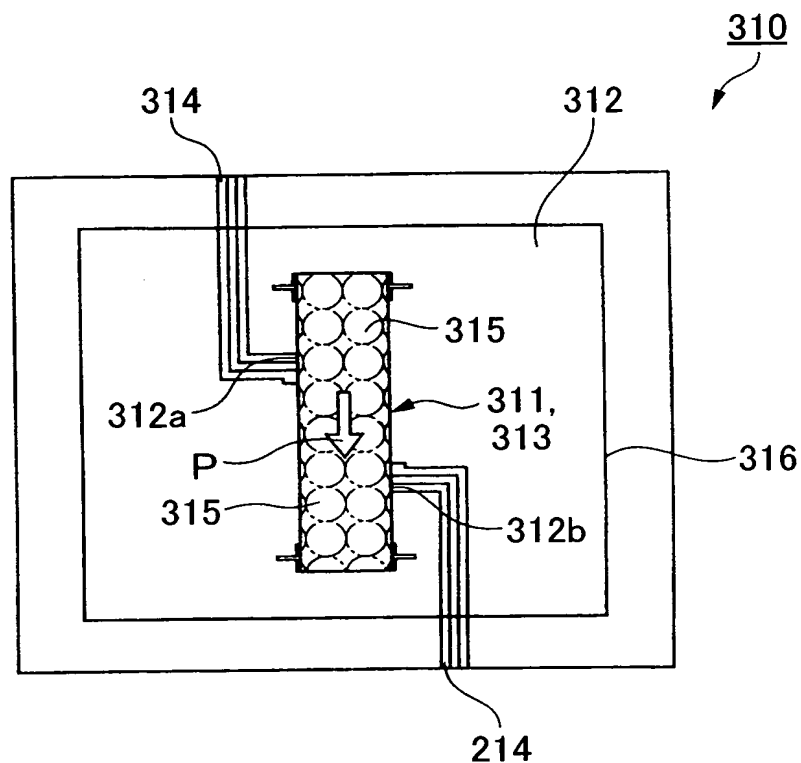


FIG. 43B

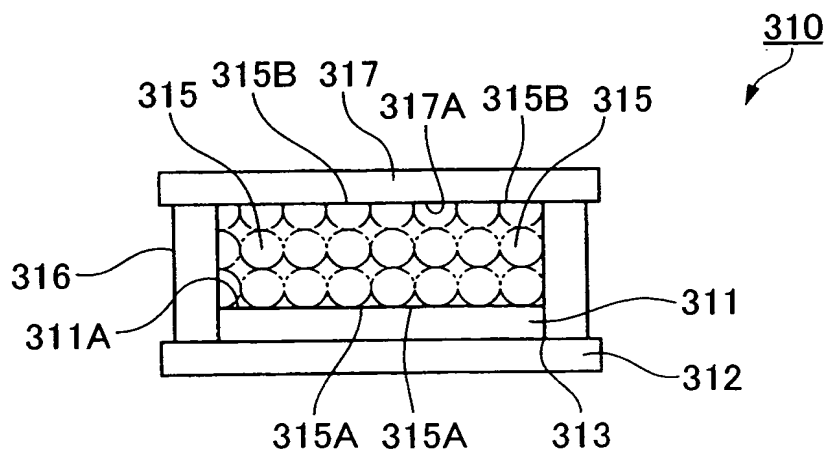


FIG. 44

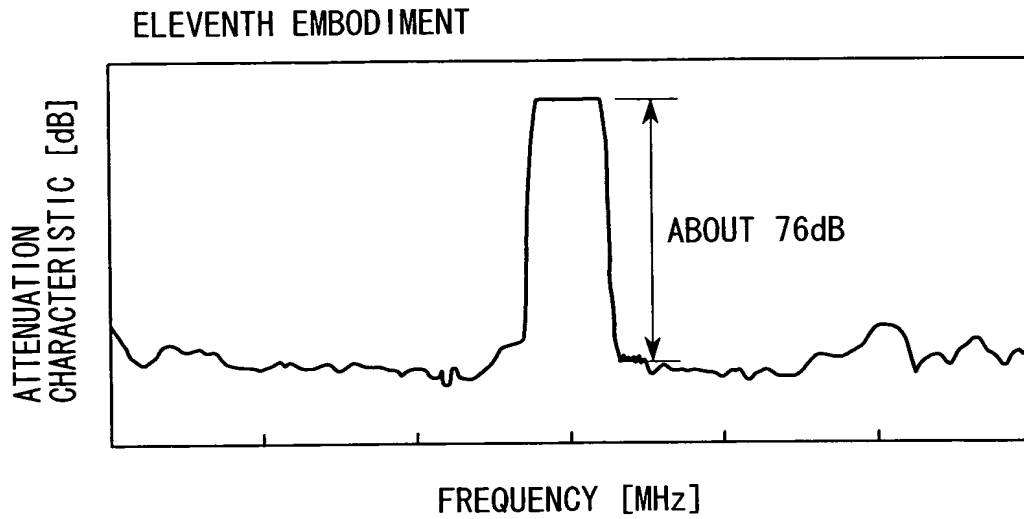


FIG. 45A

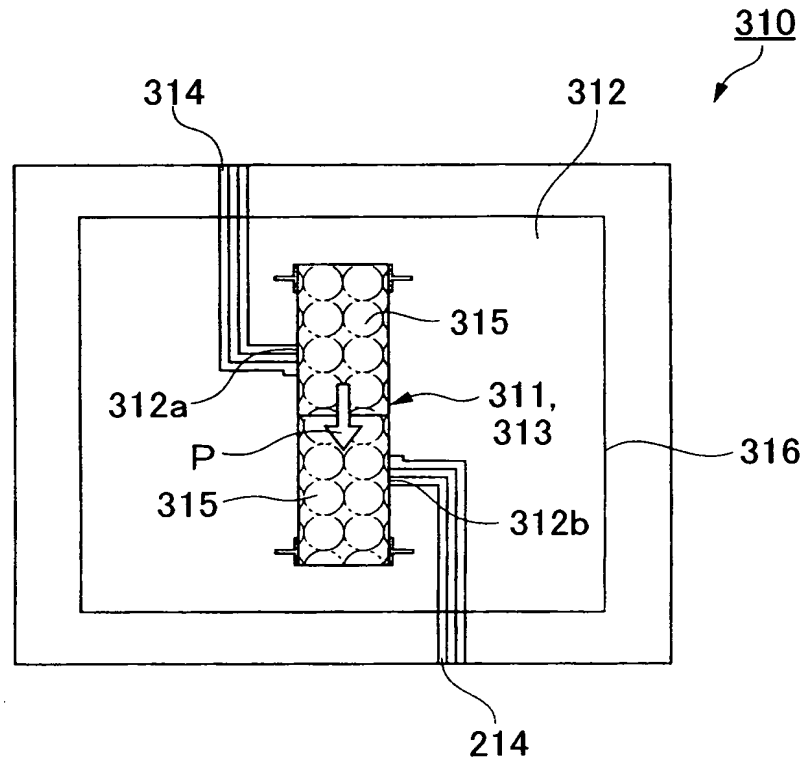


FIG. 45B

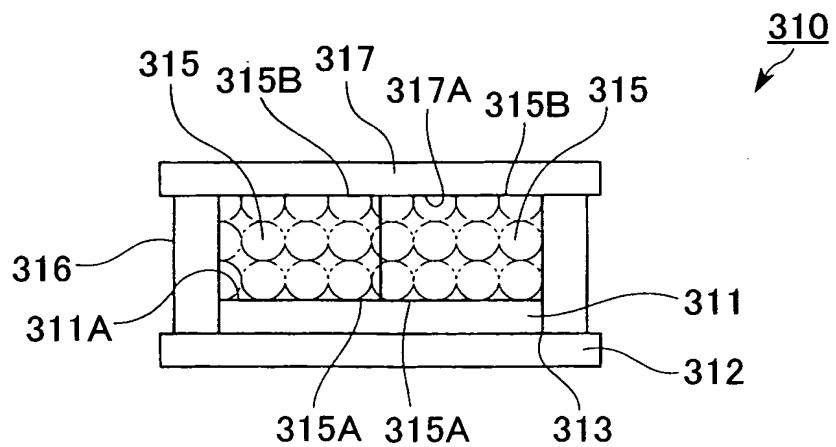
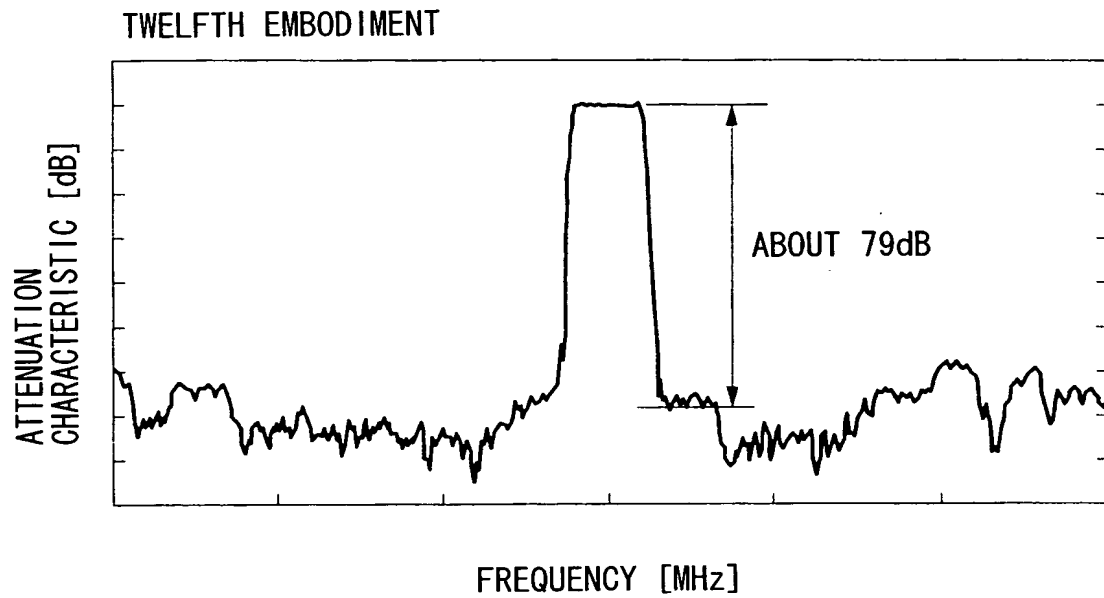


FIG. 46



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FIG. 47A

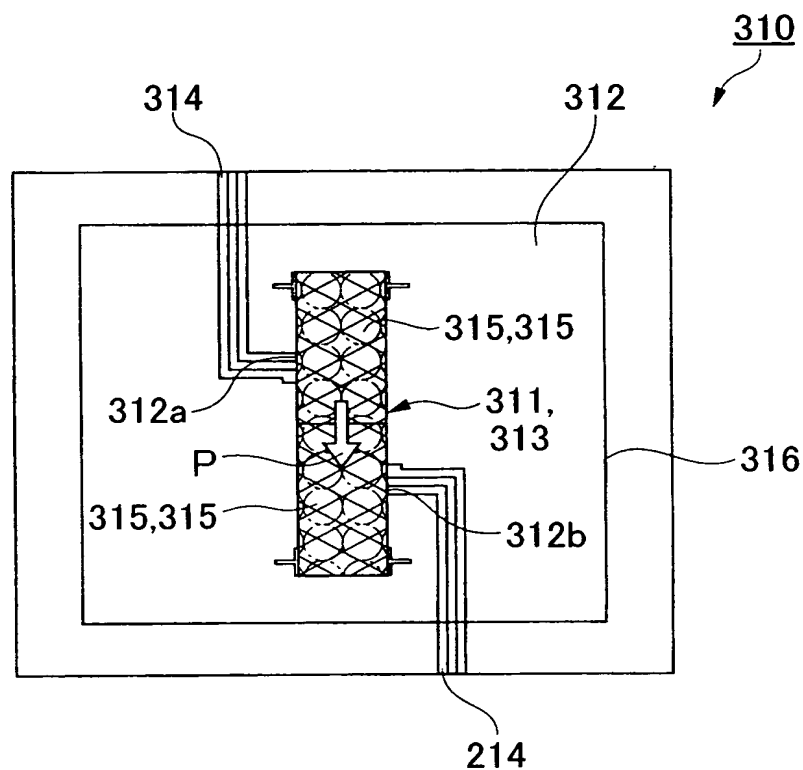
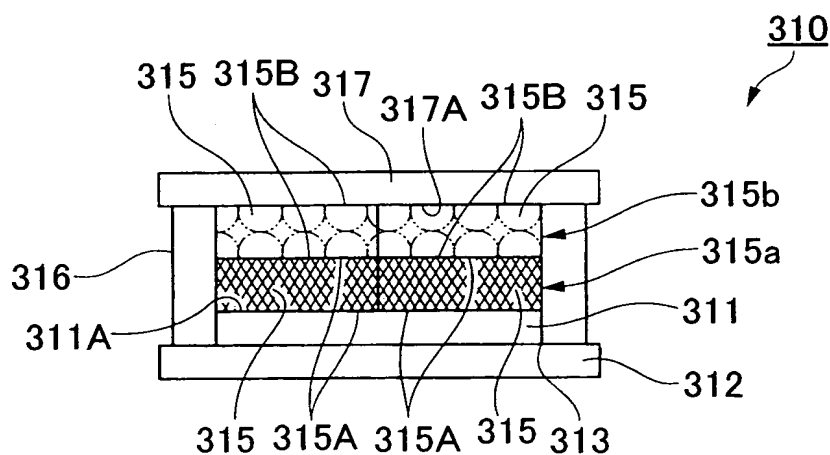


FIG. 47B



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FIG. 48

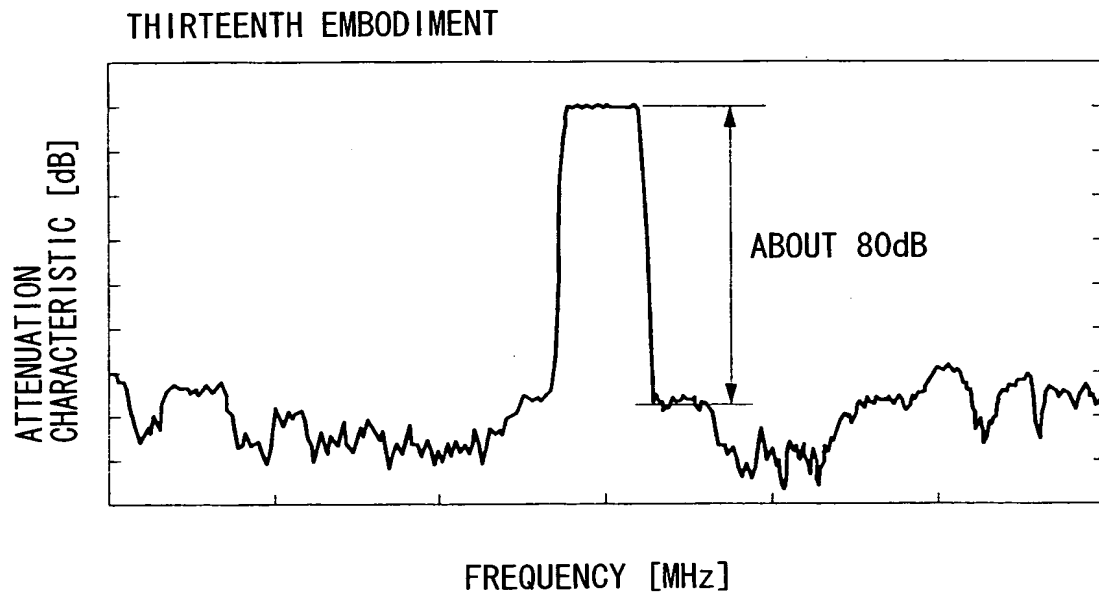


FIG. 49

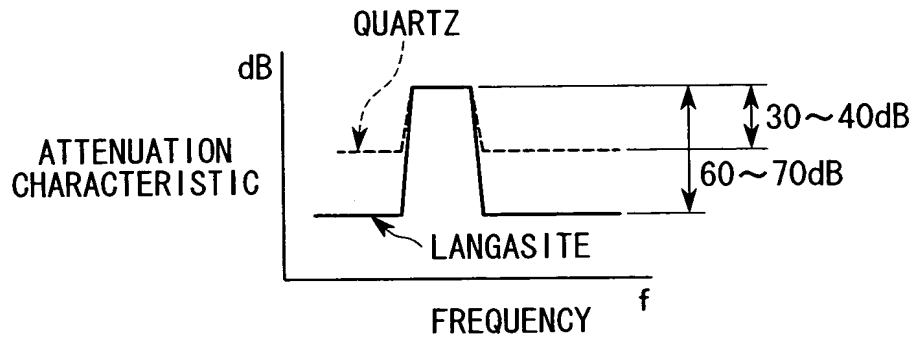


FIG. 50

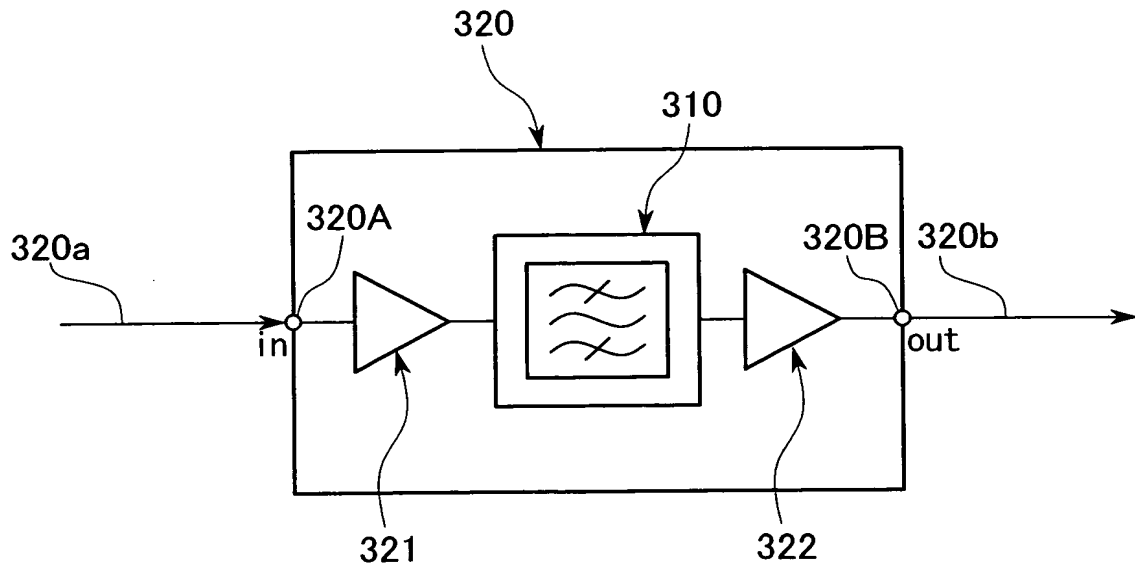




FIG. 51

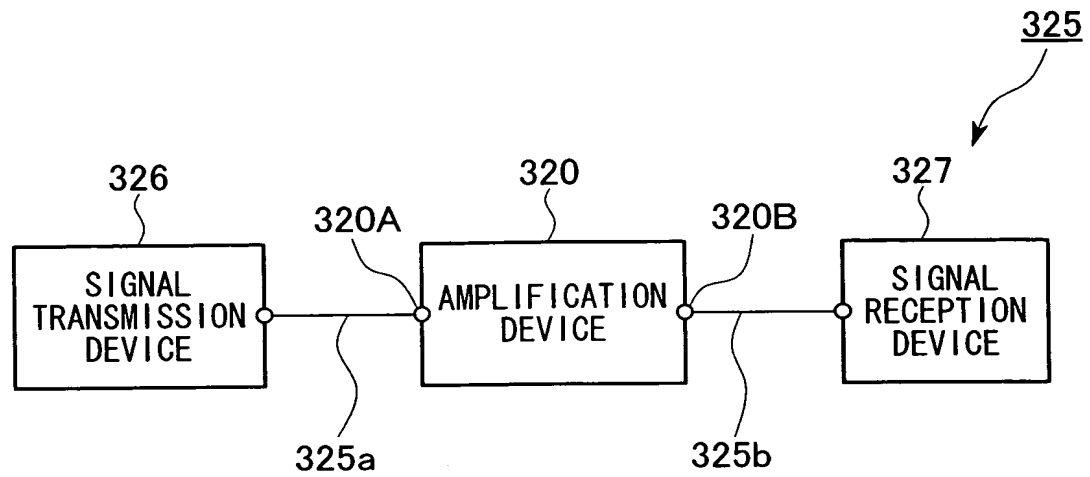
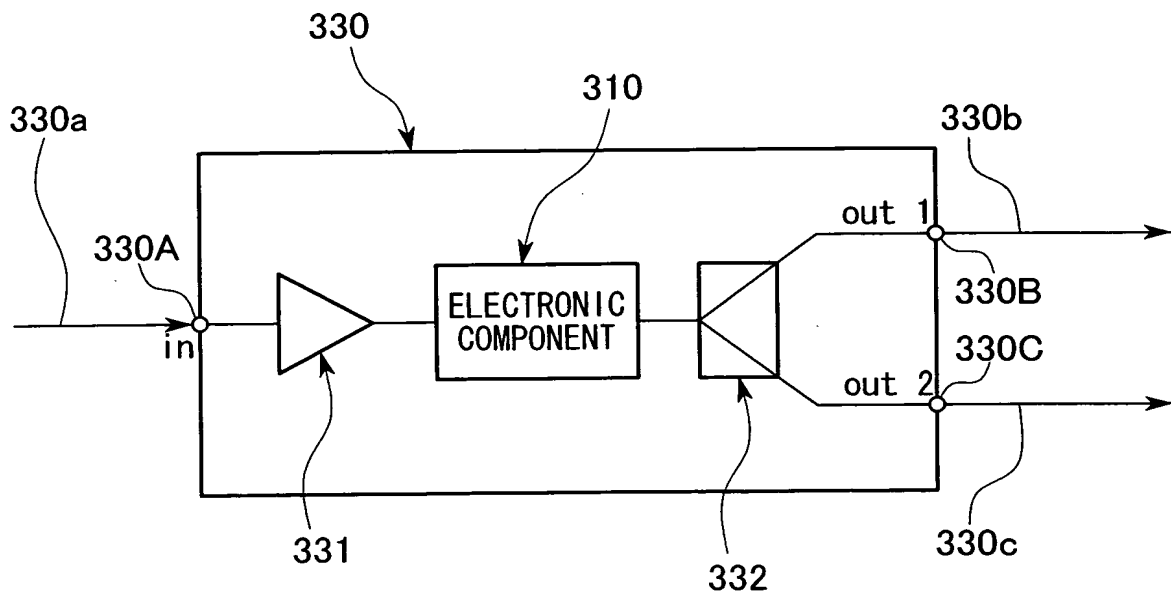


FIG. 52



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FIG. 53

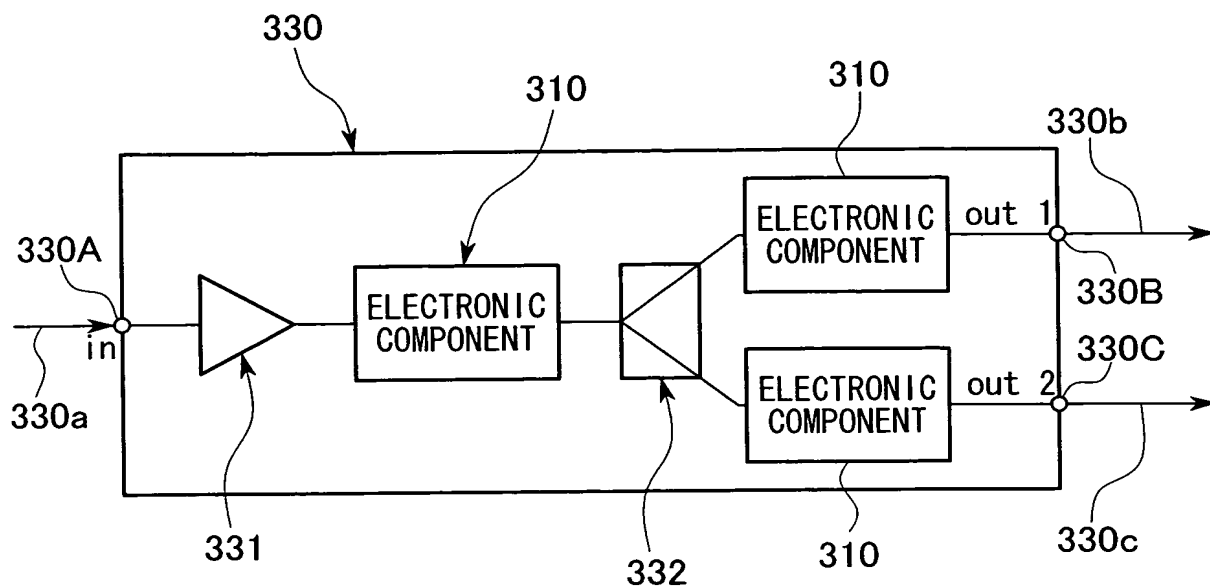


FIG. 54

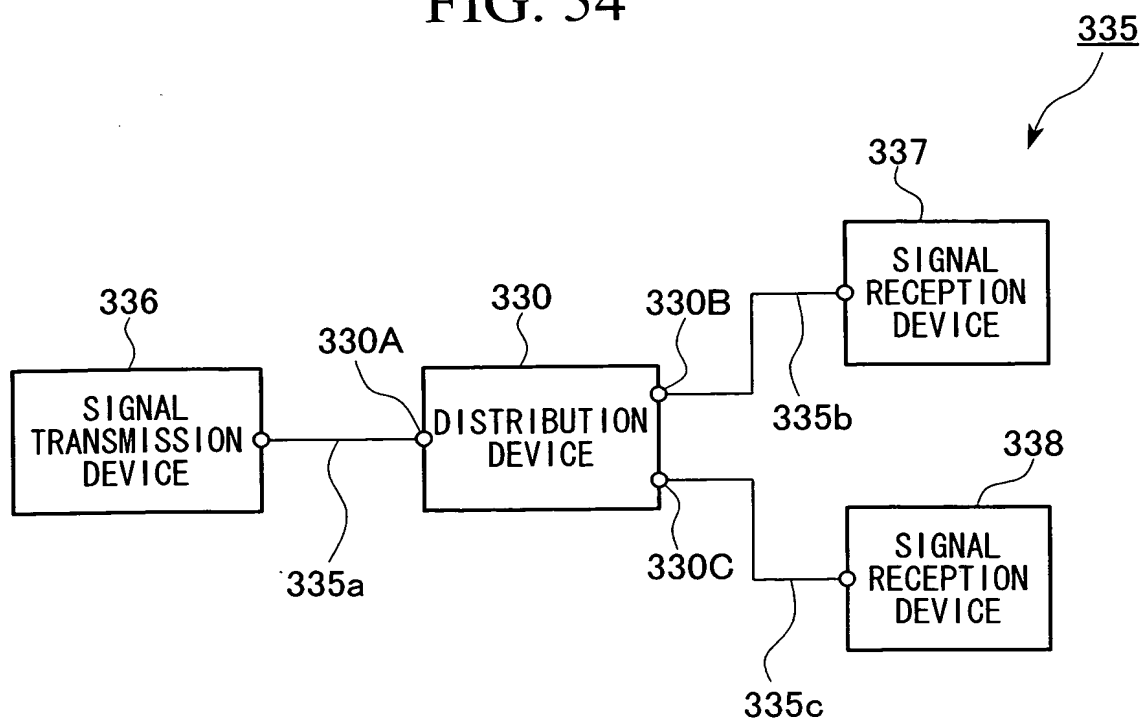


FIG. 55

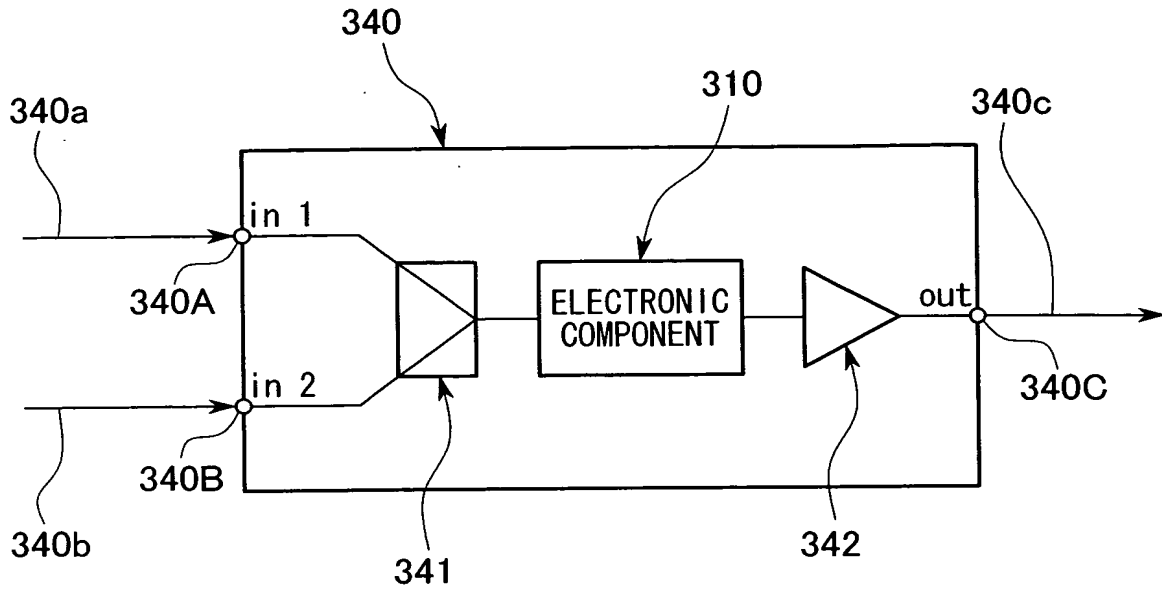


FIG. 56

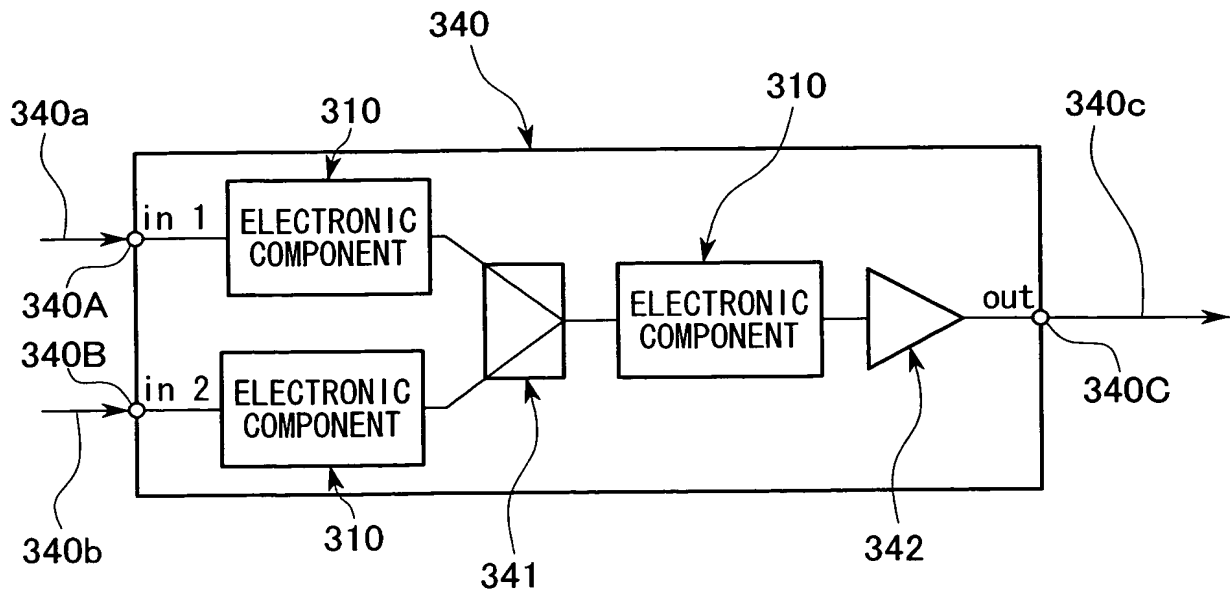


FIG. 57

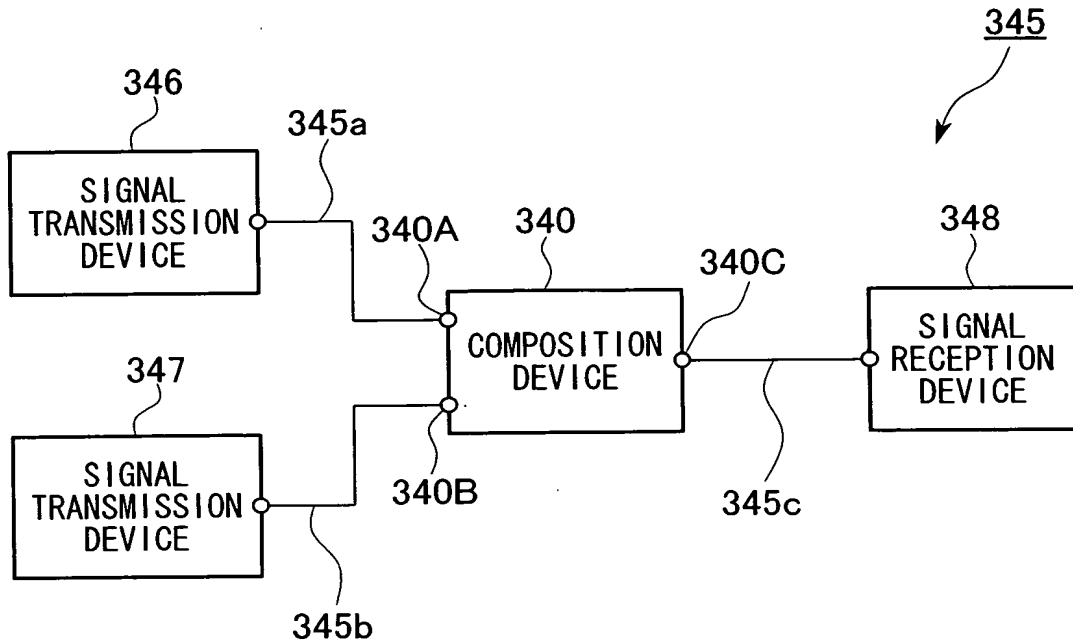


FIG. 58

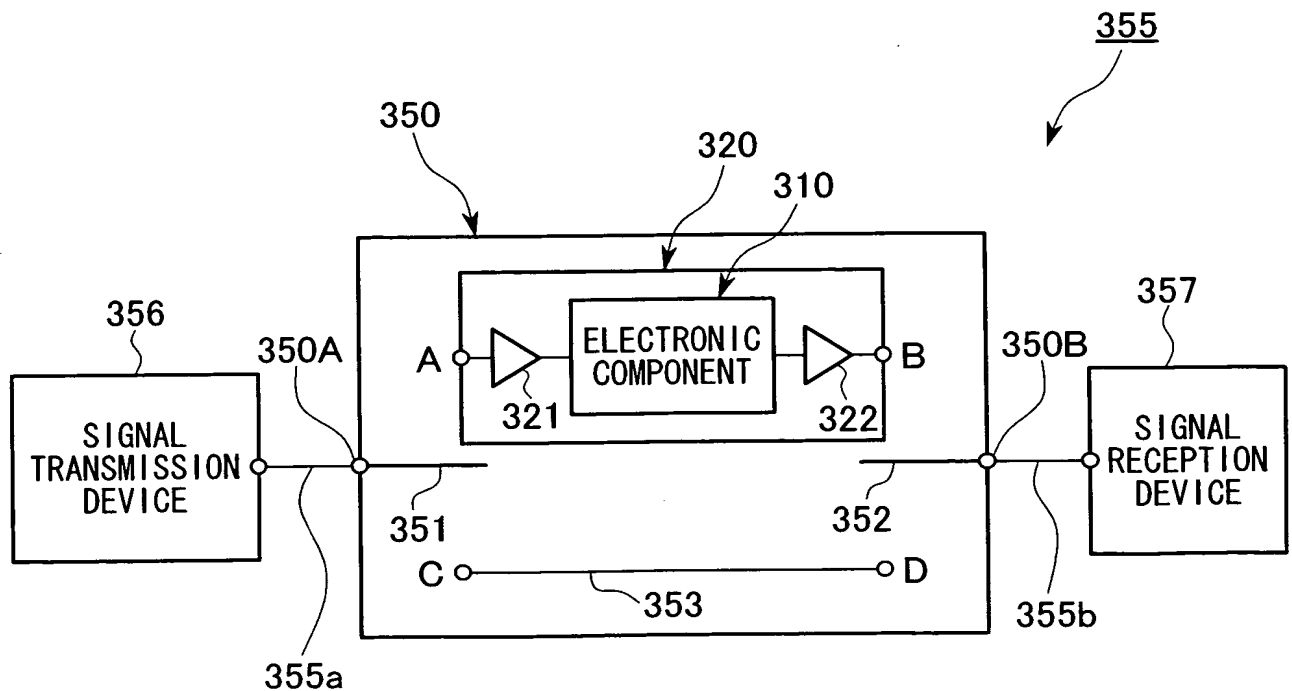


FIG. 59

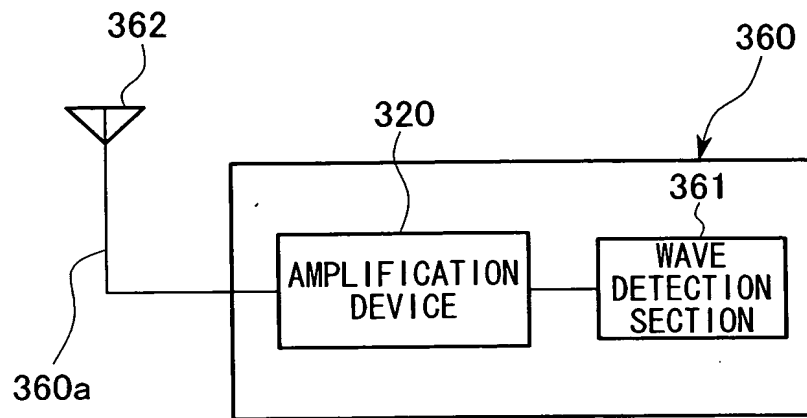


FIG. 60

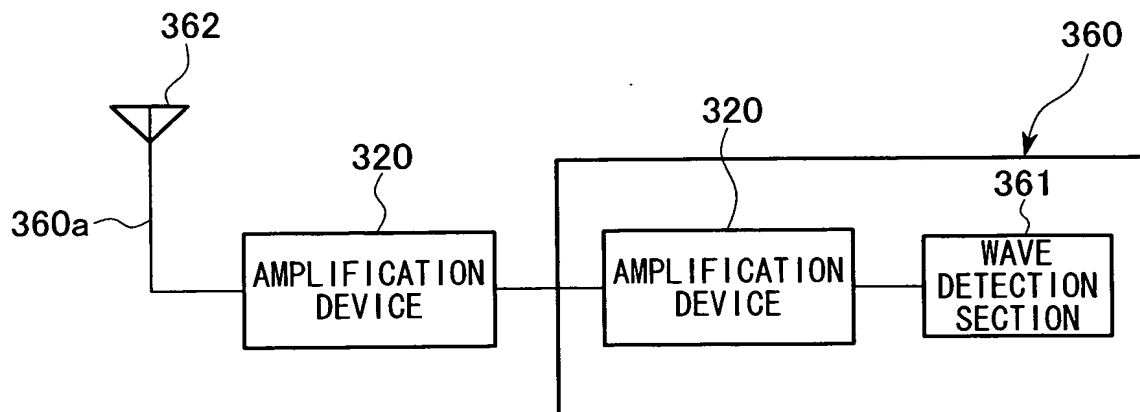


FIG. 61

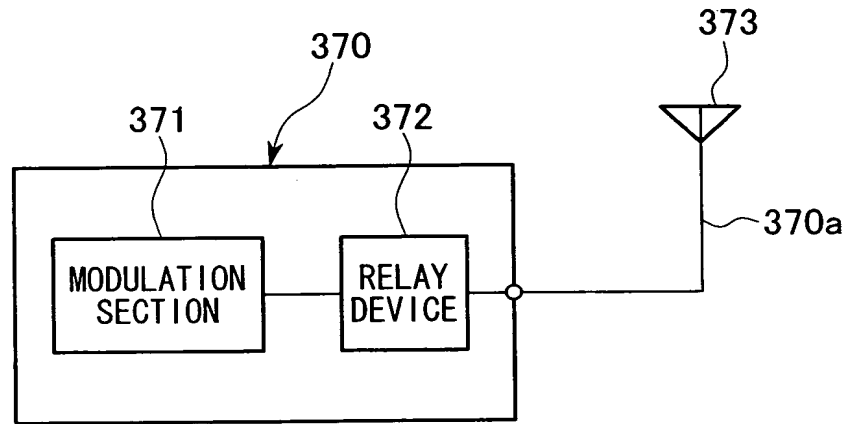
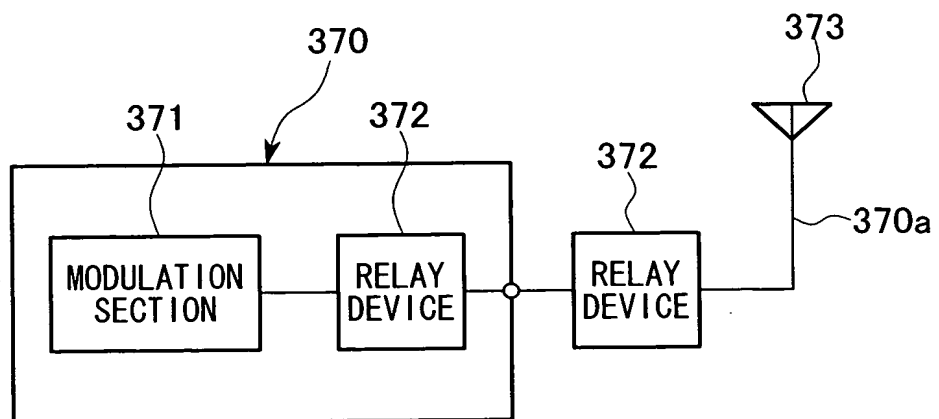


FIG. 62



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FIG. 63

